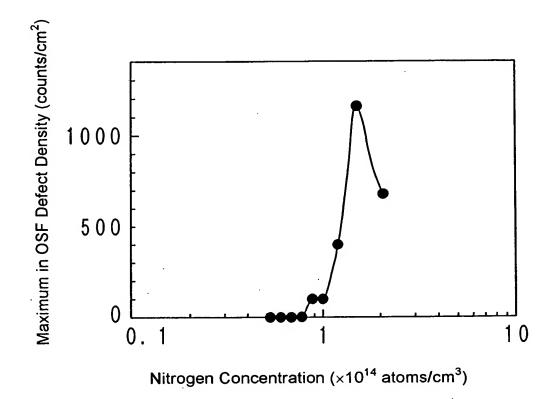
Filed: March 26, 2004
Title: SILICON ANNEALED WAFER
AND SILICON EPITAXIAL WAFER

Drawing Sheet 1 of 3

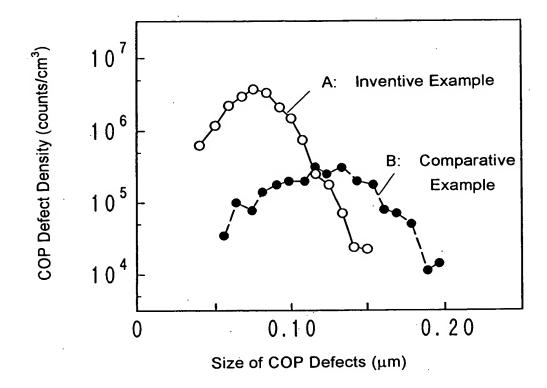
FIG. 1



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AND SILICON EPITAXIAL WAFER

Drawing Sheet 2 of 3

FIG. 2



Filed: March 26, 2004
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AND SILICON EPITAXIAL WAFER

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FIG. 3

